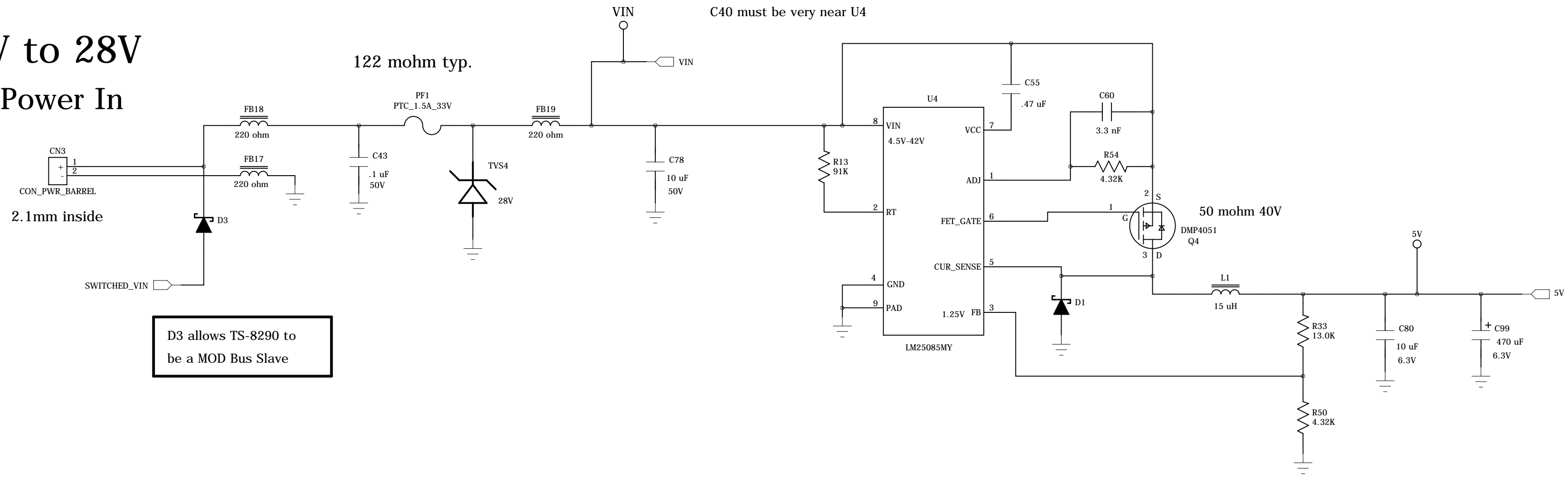


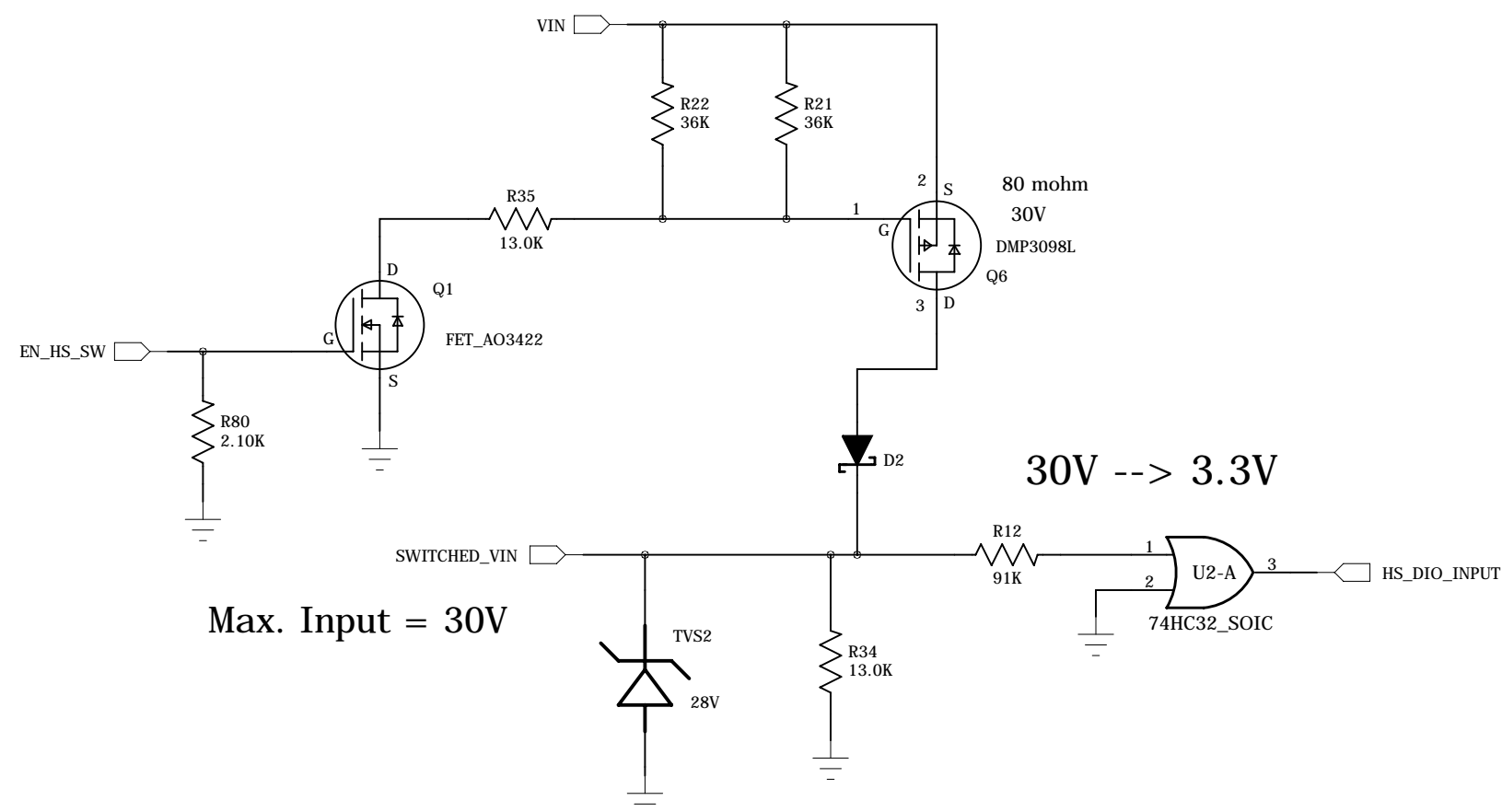
5V Power Supply (2.0 Amps)

5V to 28V
Power In



High-Side Switch

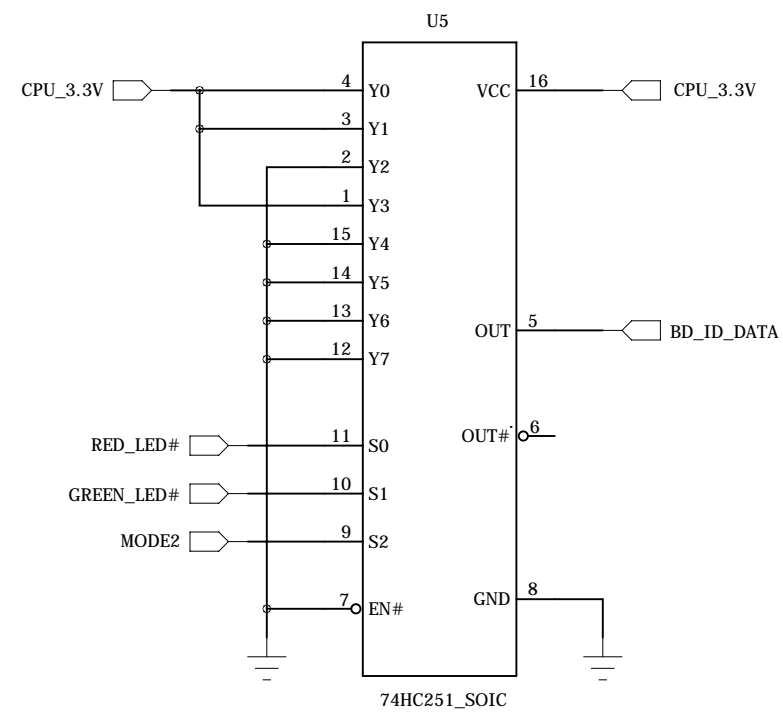
Source up to 1000 mA



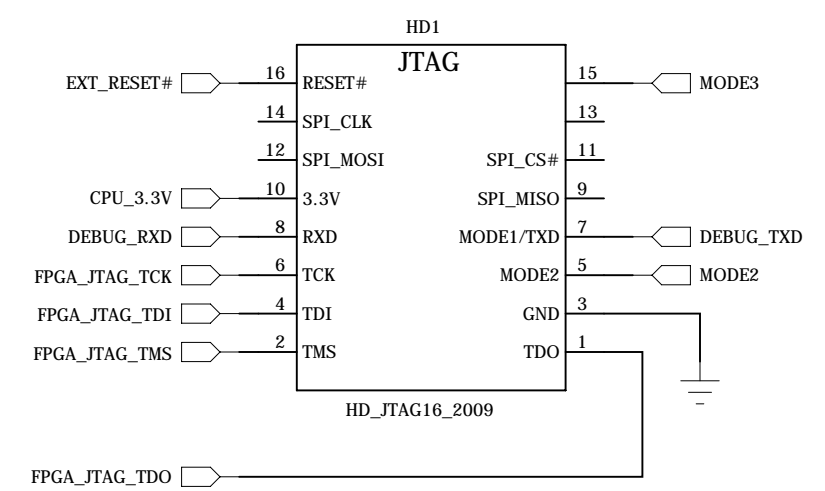
Technologic Systems	Date May 2, 2012
Title: TS-8290 5V Reg. and HS switch	
Rev: A	Designer RLM Sheet 1 of 6

Misc.

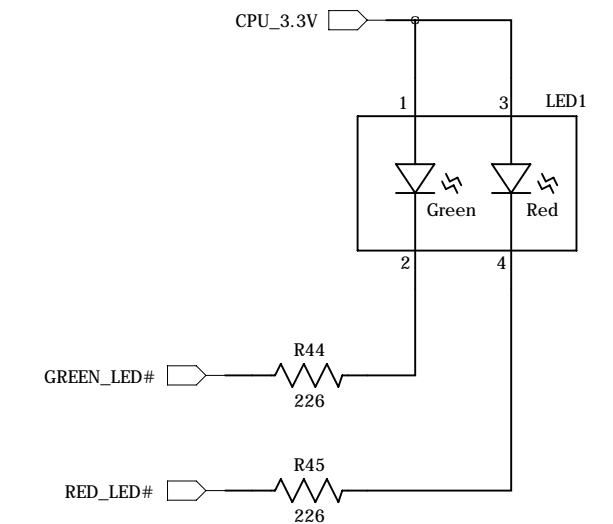
Board ID = 11



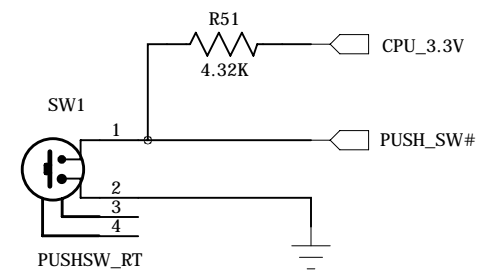
Console Header



Front Panel LEDs



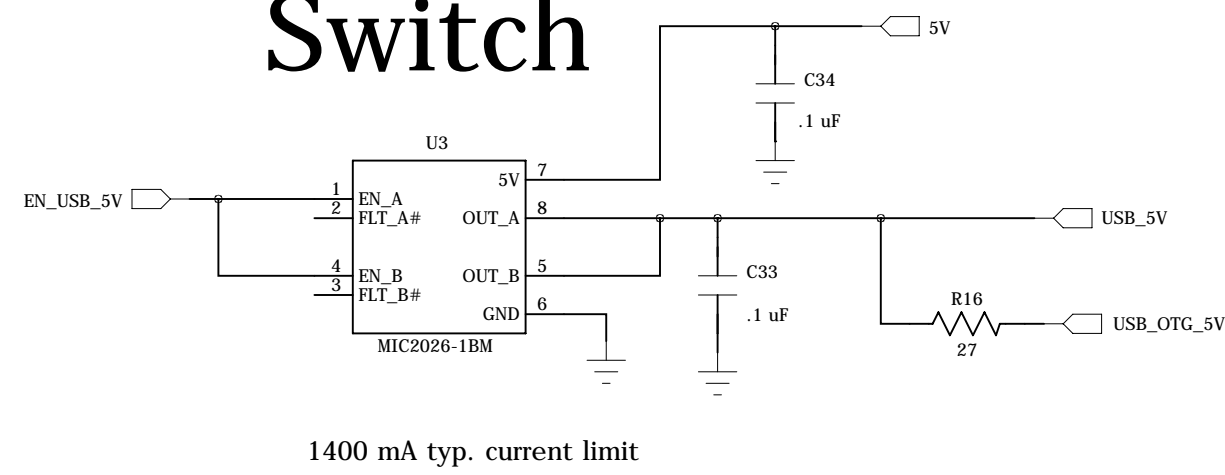
Push Switch



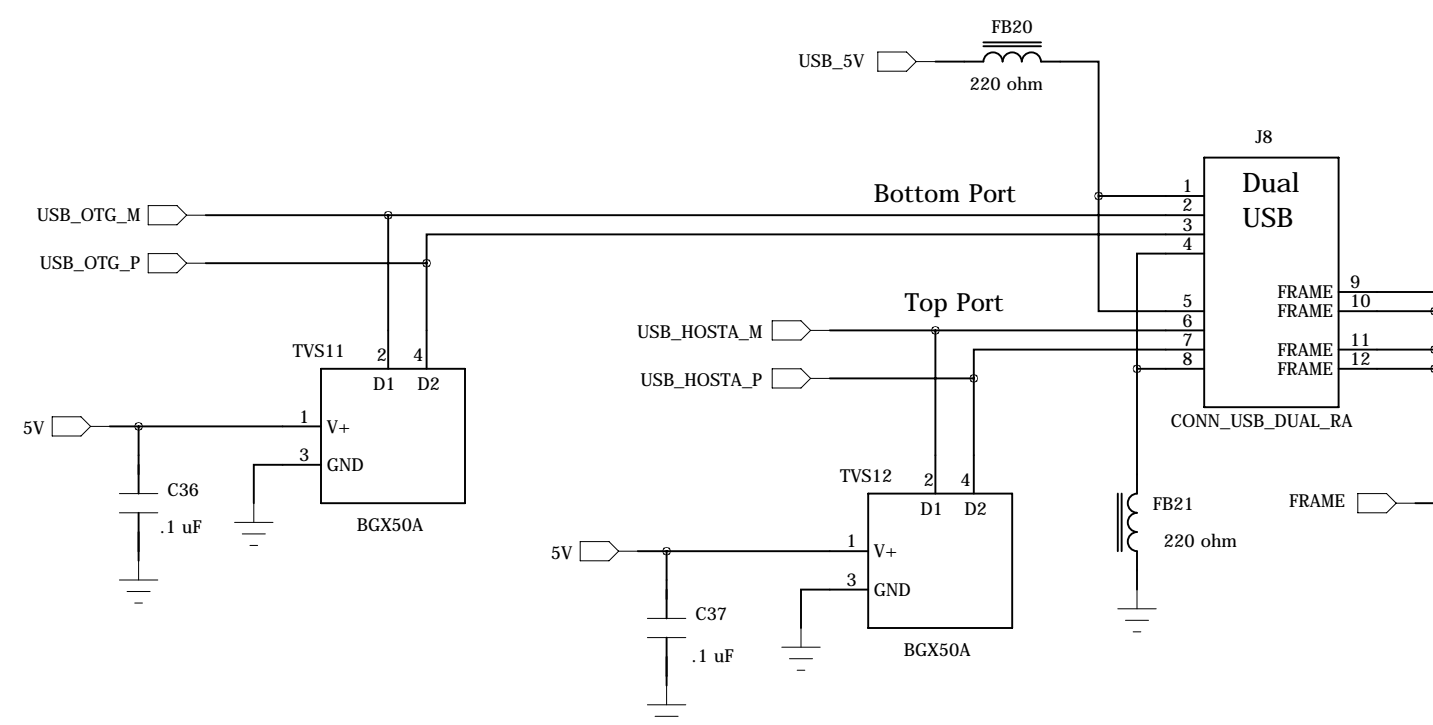
Technologic Systems	Date	May 2, 2012
Title: TS-8290 Bd ID, LEDs, Switch, etc.		
Rev: A	Designer	RLM
		Sheet 3 of 6

USB and Ethernet Ports

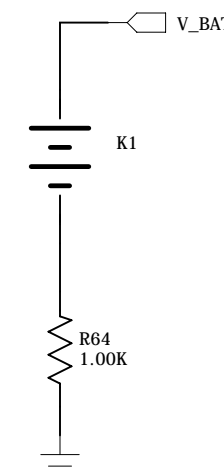
USB Power Switch



External Dual USB

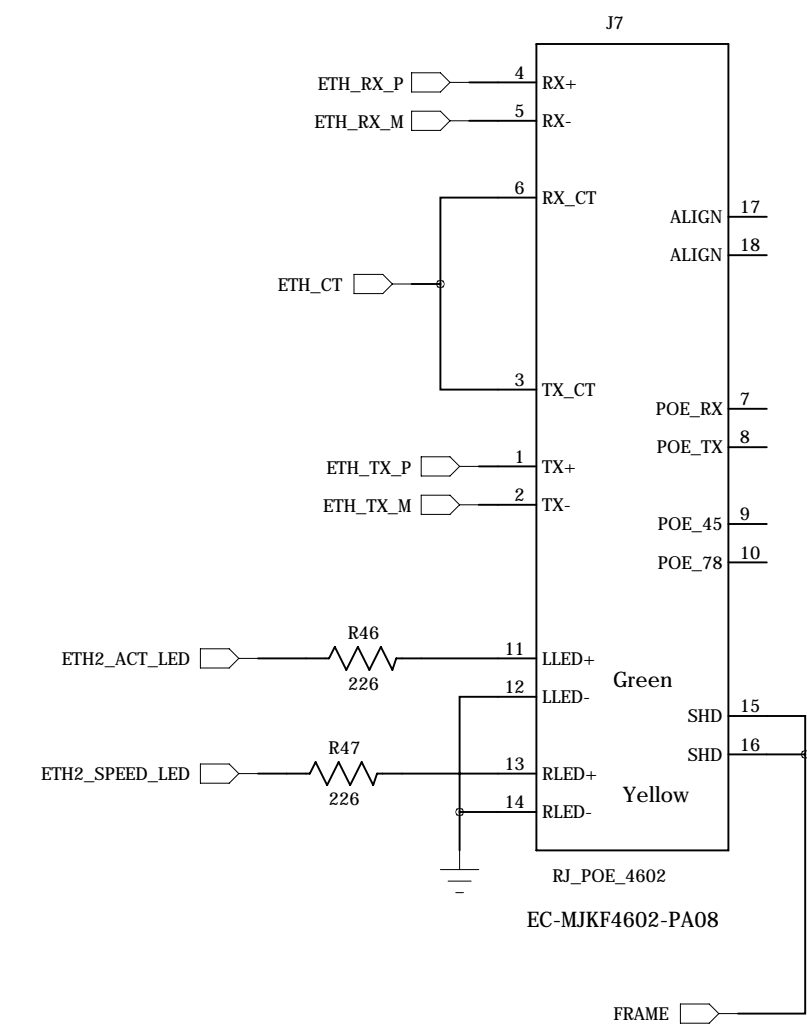


RTC Battery



SBC

10/100 Ethernet



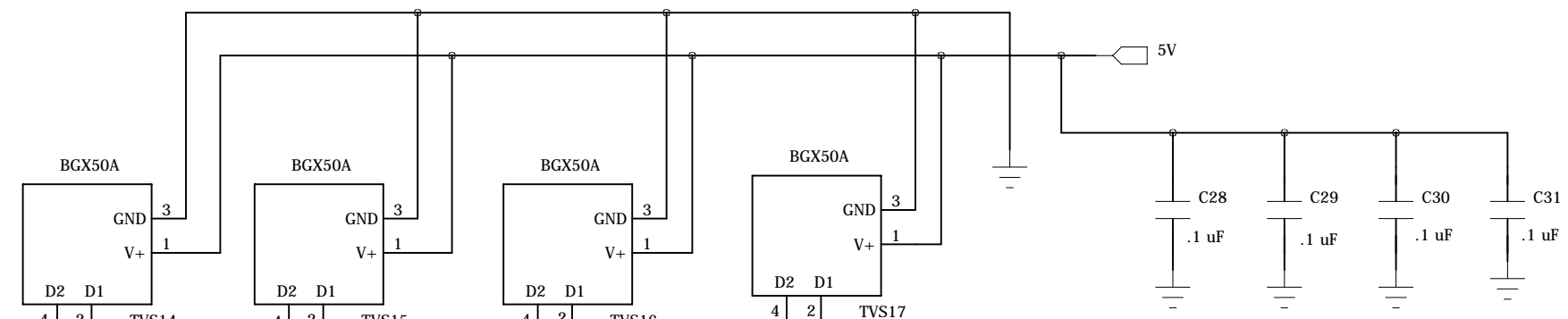
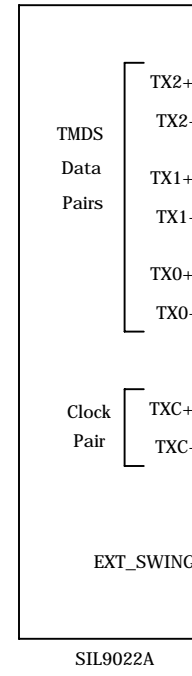
Technologic Systems	Date May 2, 2012
Title: TS-8290 USB, Battery, and Ethernet	
Rev: A	Designer RLM Sheet 4 of 6

HDMI

TMDS pairs do not need to have matched lengths

100 ohm impedance

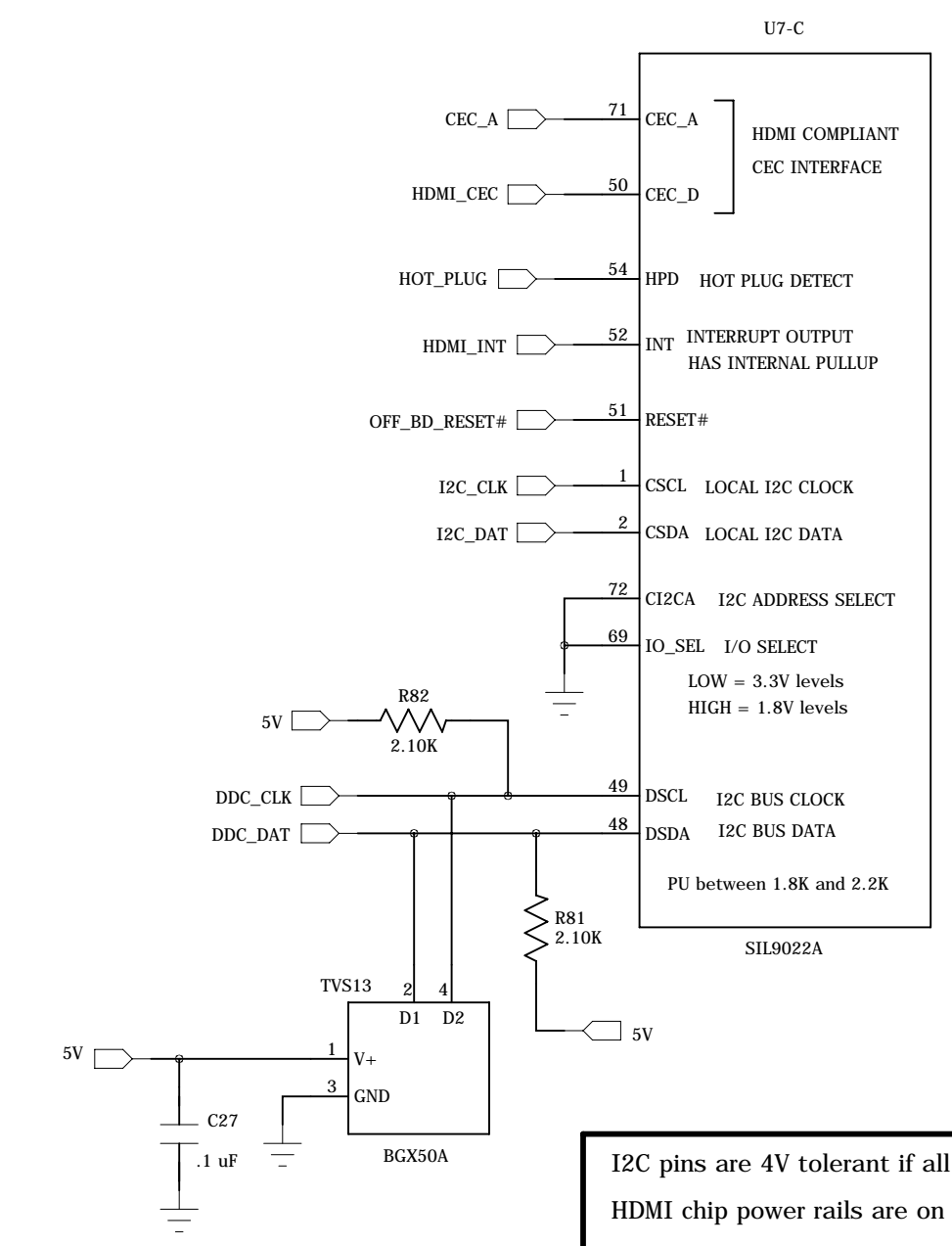
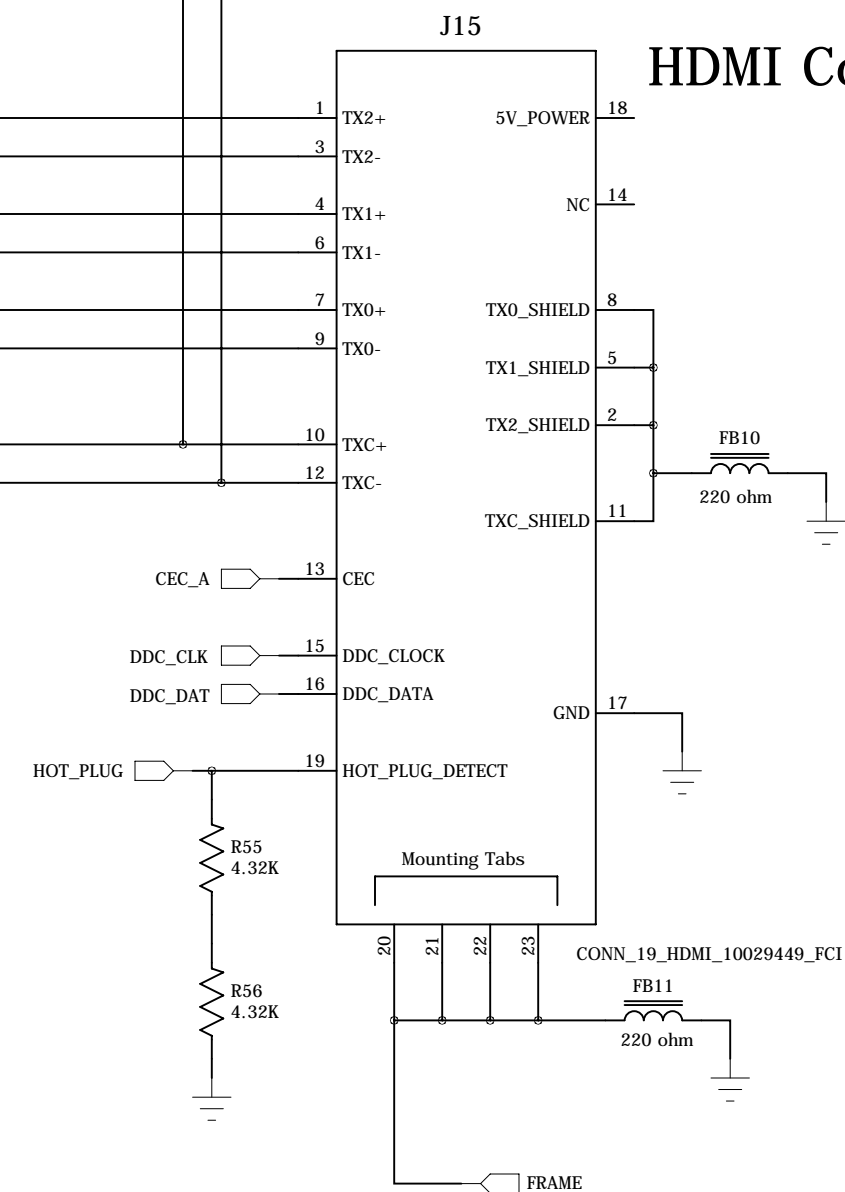
U7-D
Video Out



4.32K correct if Internal termination resistors enabled

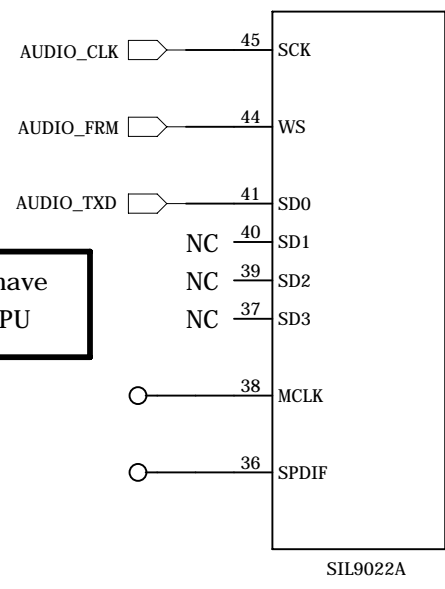
Internal termination required for > 100 MHz

HDMI Connector



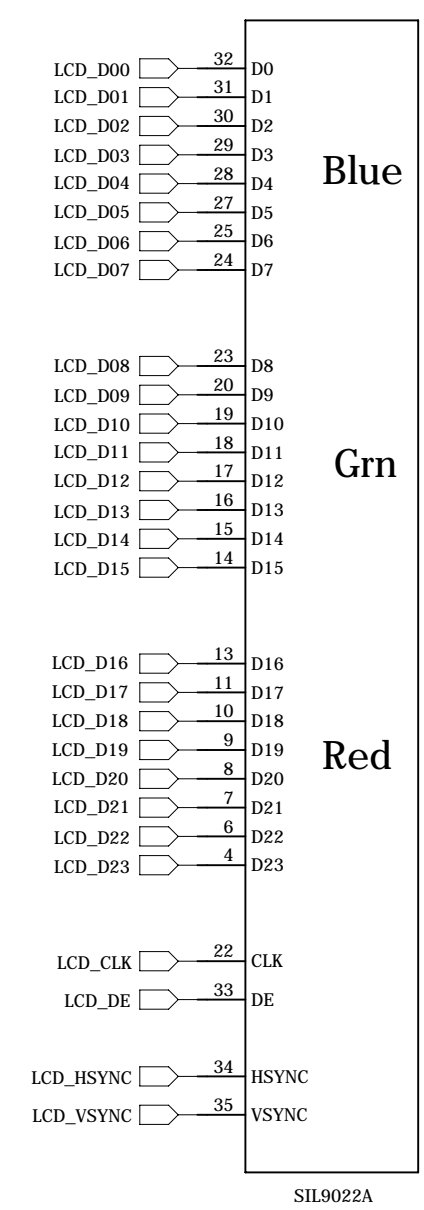
I2C pins are 4V tolerant if all HDMI chip power rails are on

U7-B
AUDIO INPUT



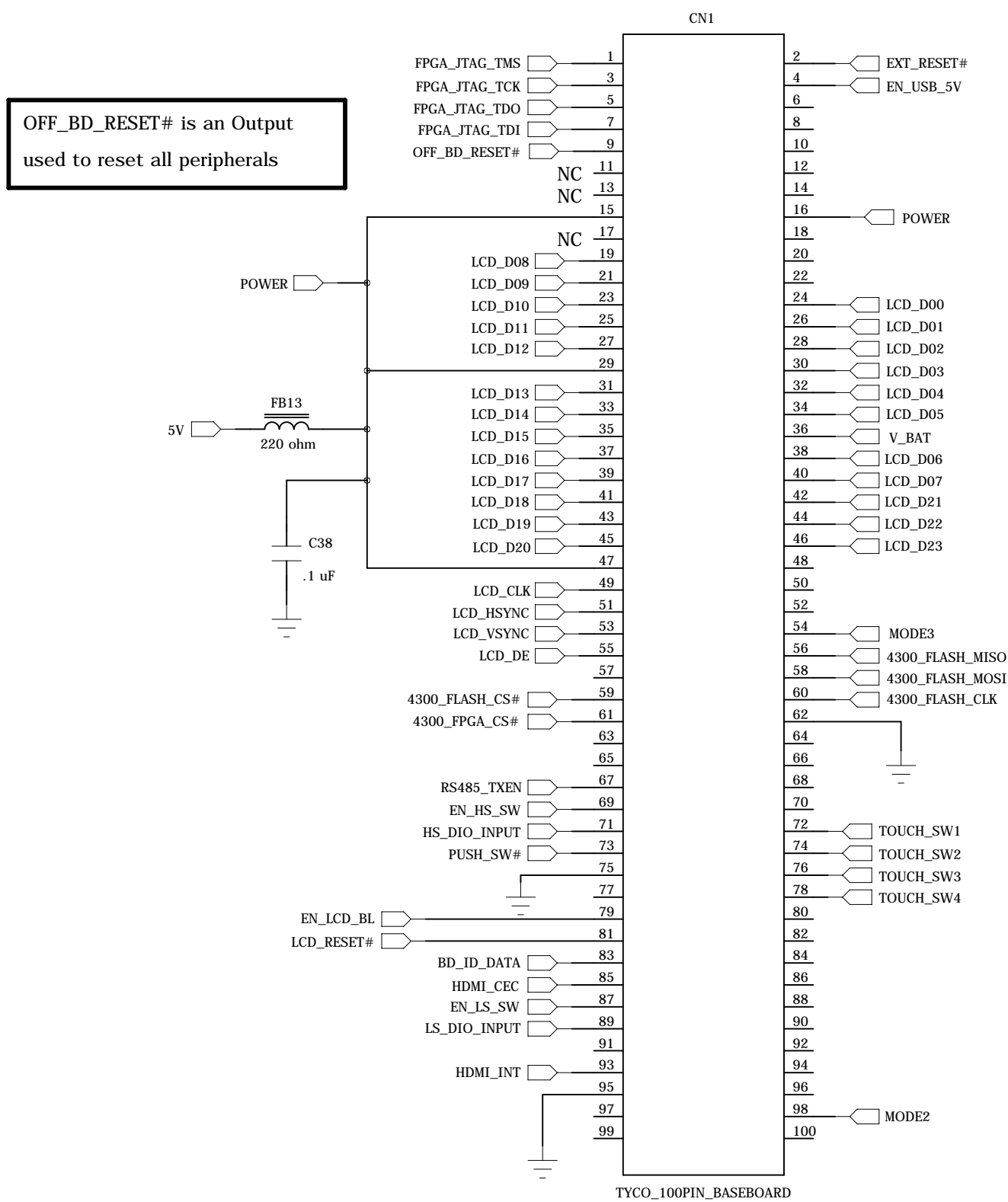
These signals have weak internal PU

U7-A
VIDEO INPUT



Two 100-pin Module Connectors

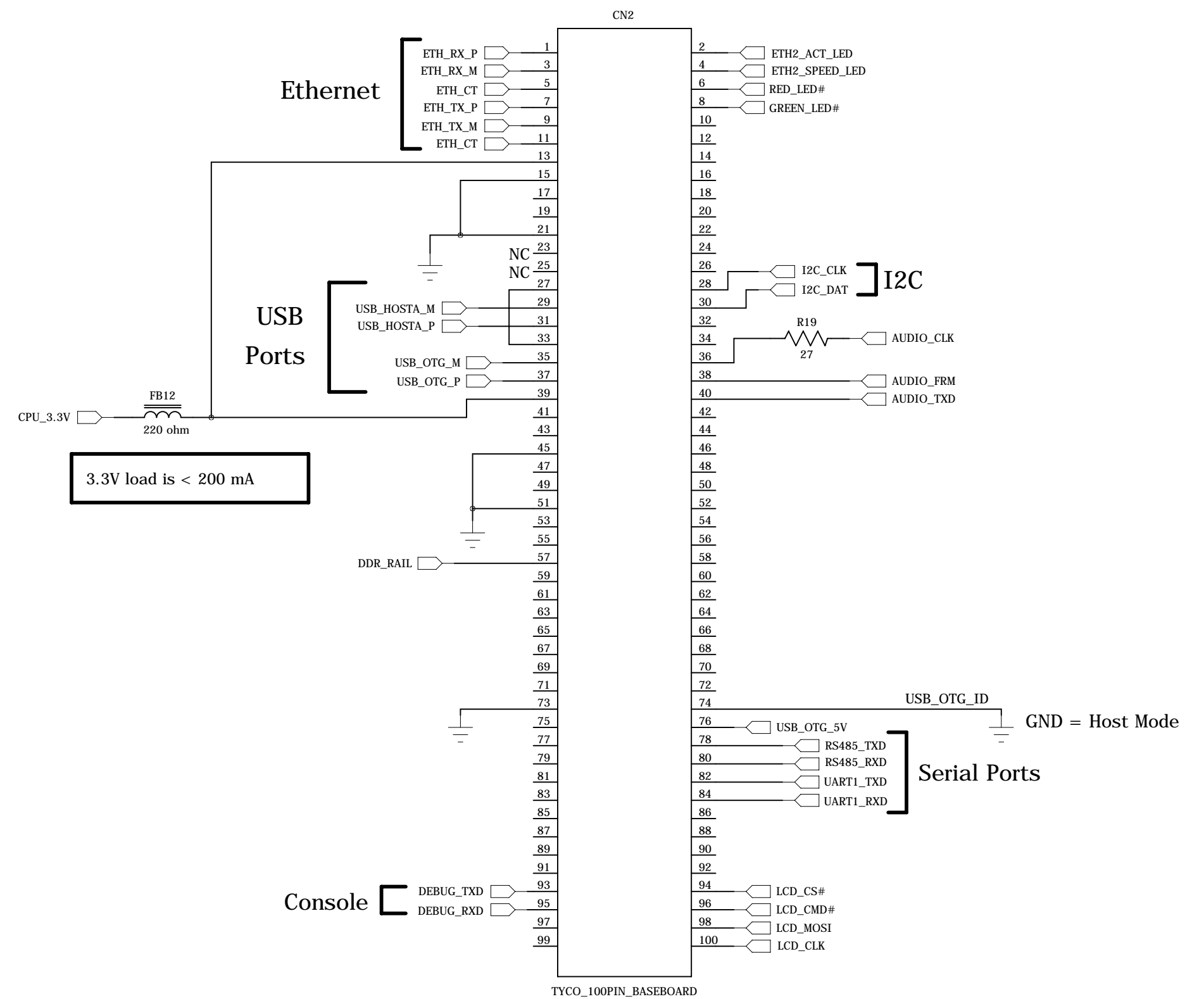
Left



OFF_BD_RESET# is an Output used to reset all peripherals

EXT_RESET# is an Input used to reboot the CPU
Do not drive active high (use open drain)

Right



Ethernet

USB Ports

Console

I2C

Serial Ports

Boot Strap

Mode 2	SBC Boots from
1	NAND Flash
0	SD Card

MODE1 and MODE2 states are latched prior to OFF_BD_RESET# deasserted

MODE1 and MODE2 have PU resistors on the SBC module