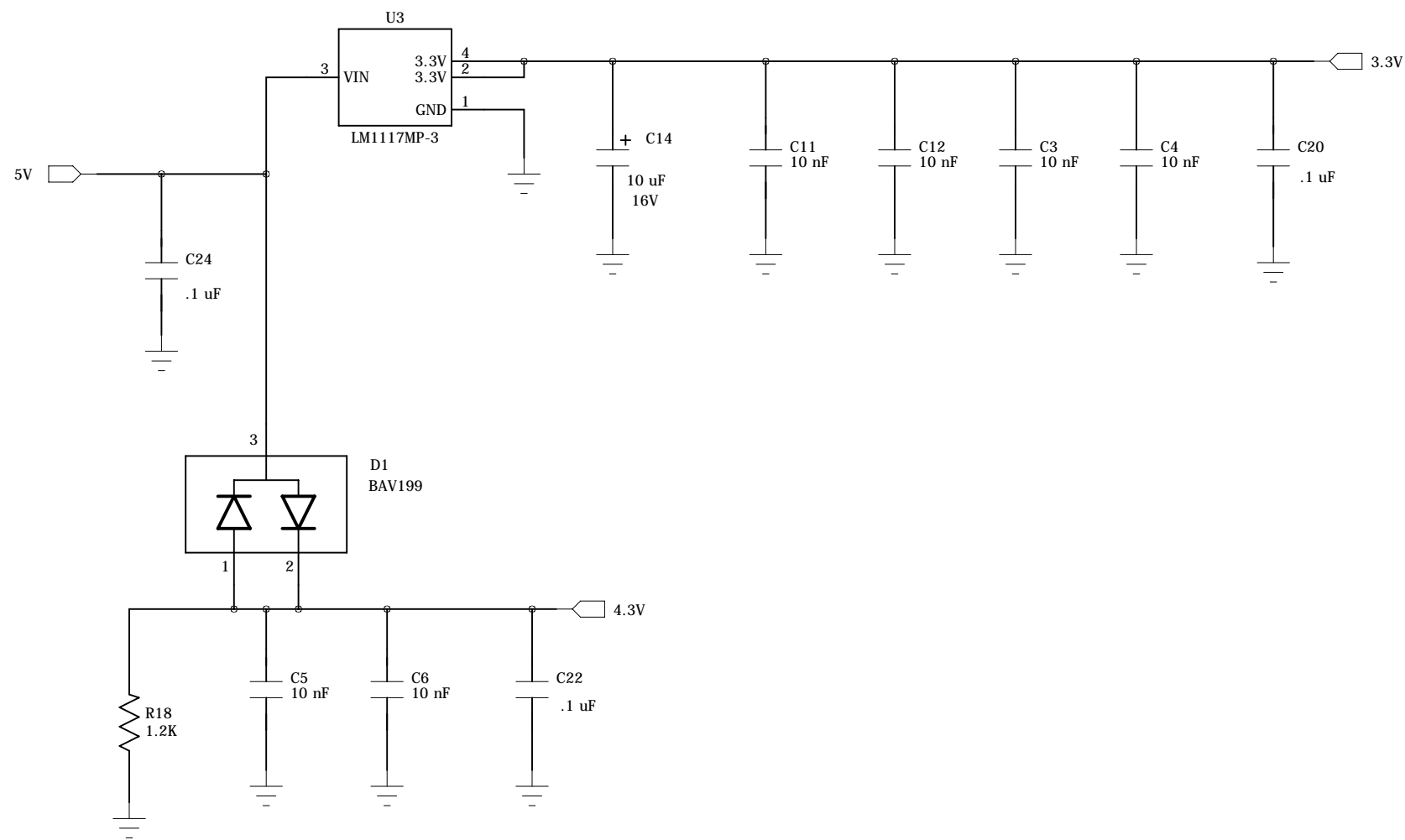
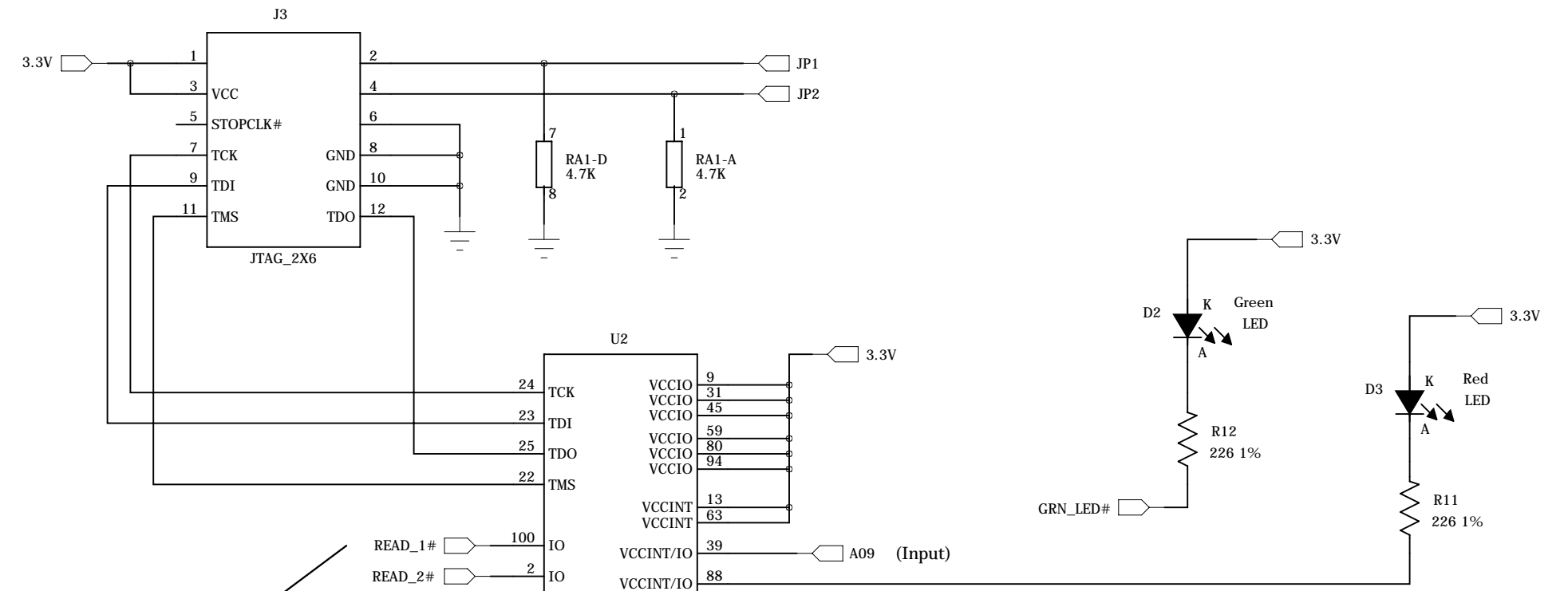


### 3.3V Reg.



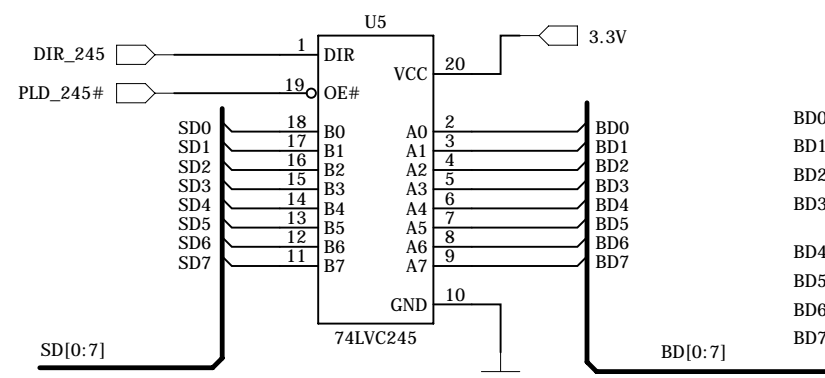
### JTAG



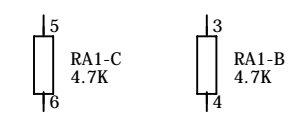
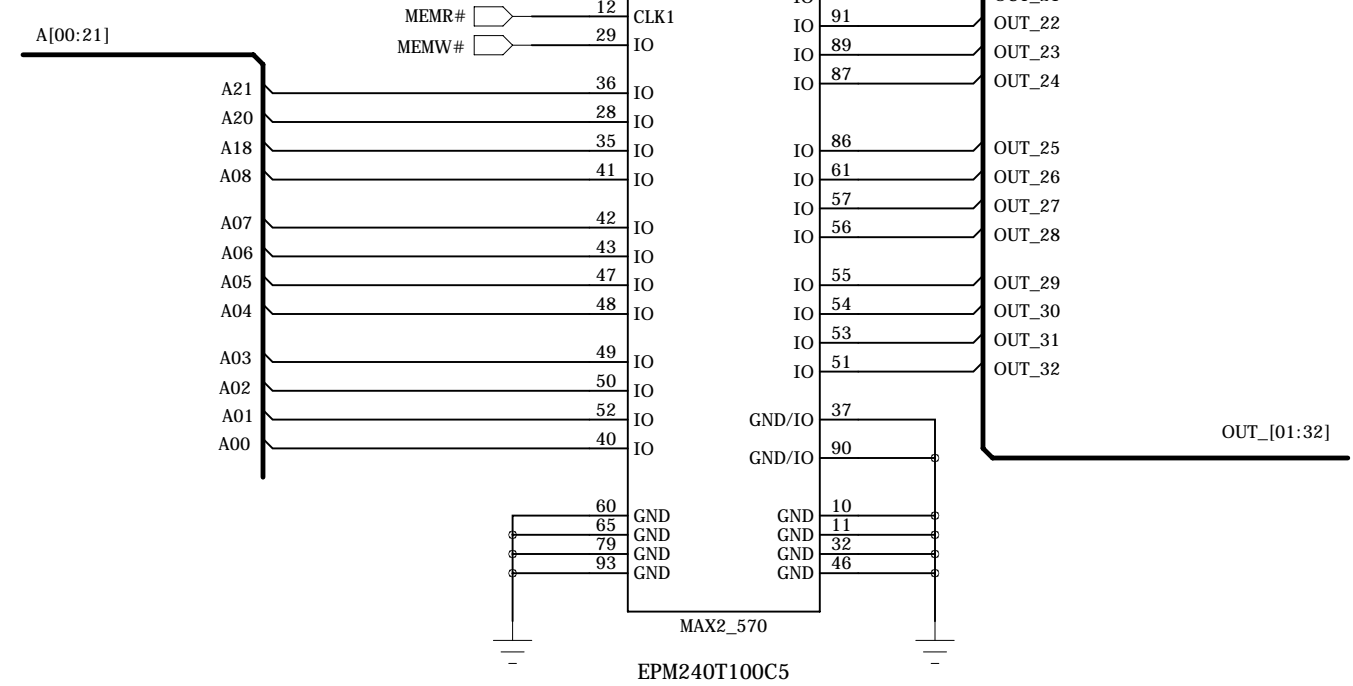
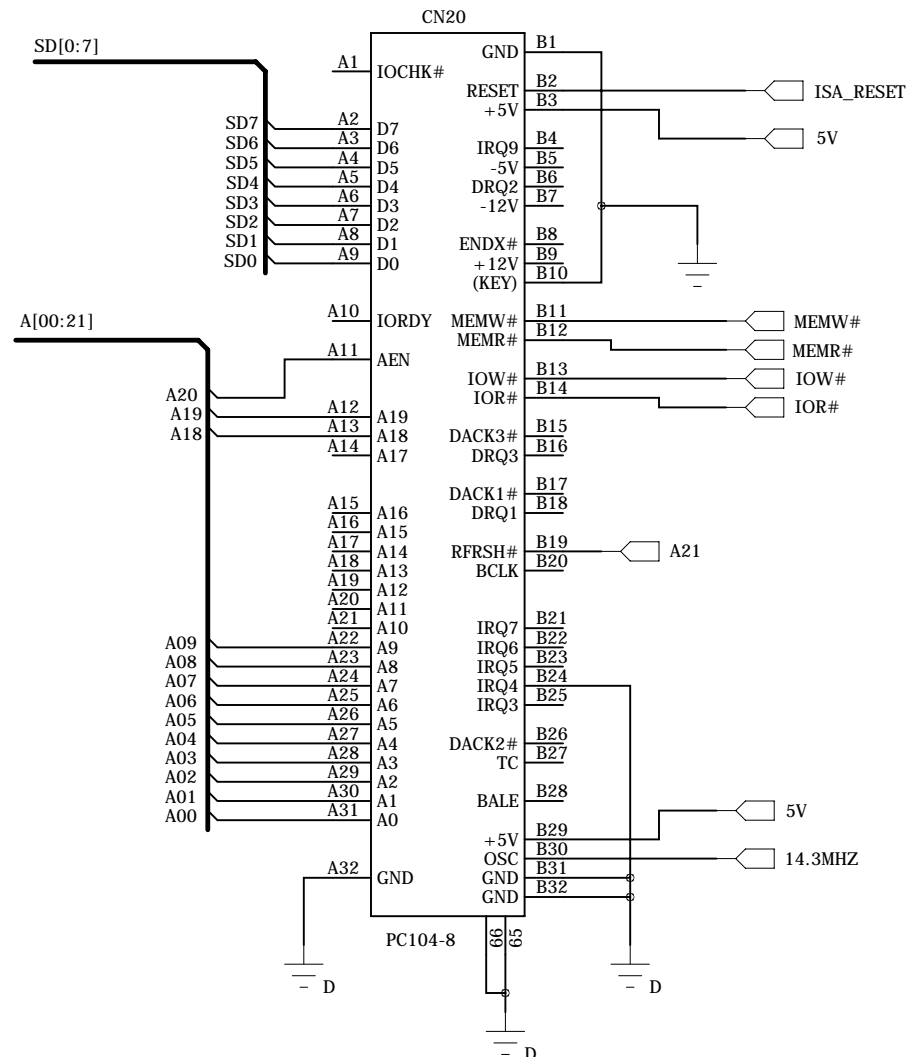
### Outputs

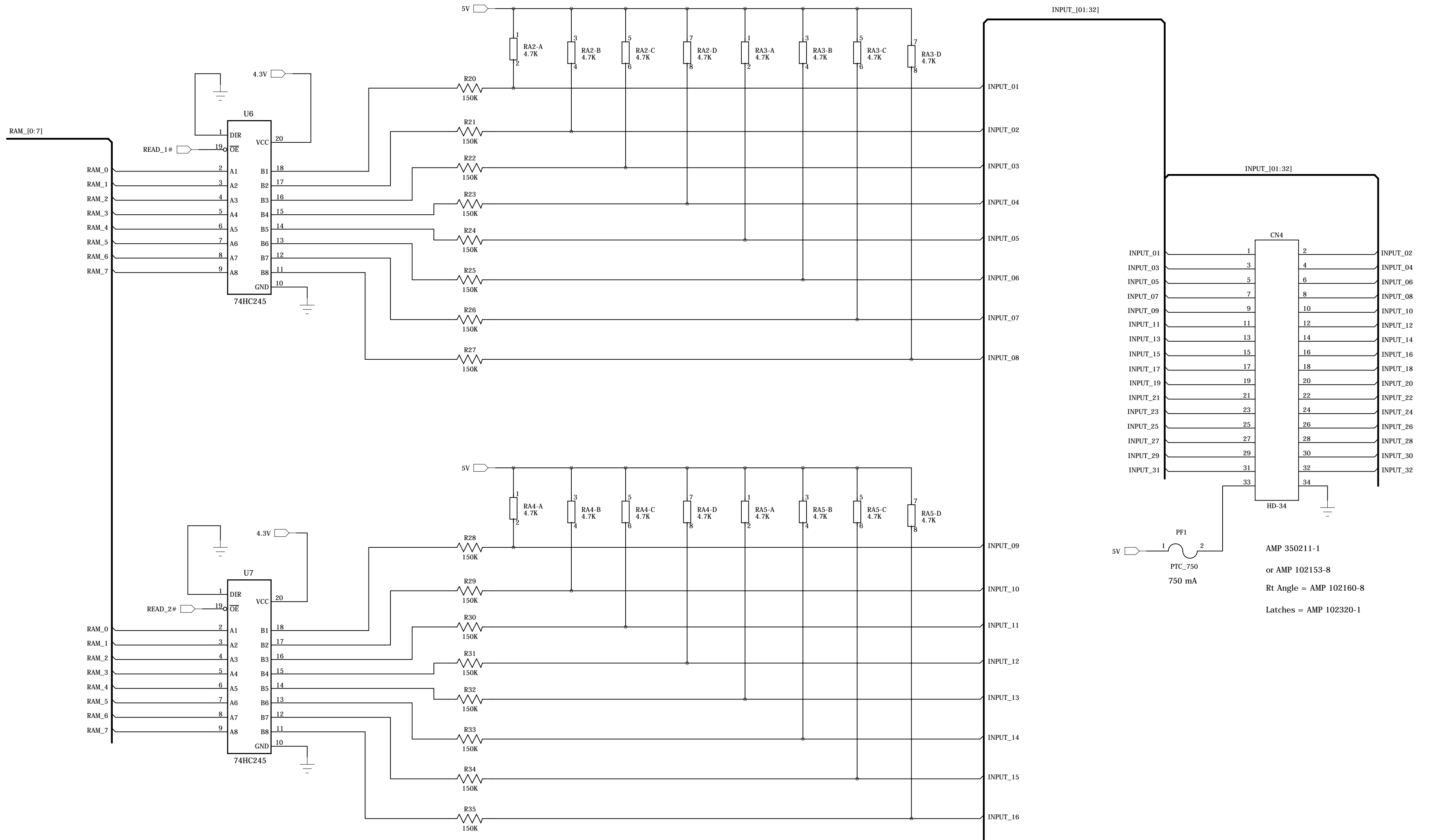
- READ\_1# 100 IO
- READ\_2# 2 IO
- READ\_3# 1 IO
- READ\_4# 99 IO
- GATED\_A18 20 IO
- RAM\_RD# 3 IO
- RAM\_WR# 26 IO
- GRN\_LED# 83 IO
- 14.3MHZ 62 CLK3
- JP1 4 IO
- JP2 38 IO
- DS\_RST# 97 IO
- BATT\_FAIL# 92 IO
- ISA\_RESET 18 IO
- RAM\_CS# 98 IO
- DIR\_245 19 IO
- PLD\_245# 21 IO
- RAM\_245# 27 IO
- OUT\_01 58 IO
- OUT\_02 44 IO
- OUT\_03 85 IO
- OUT\_04 84 IO
- OUT\_05 81 IO
- OUT\_06 30 IO
- OUT\_07 33 IO
- OUT\_08 82 IO
- OUT\_09 78 IO
- OUT\_10 77 IO
- OUT\_11 76 IO
- OUT\_12 75 IO
- OUT\_13 74 IO
- OUT\_14 73 IO
- OUT\_15 72 IO
- OUT\_16 71 IO
- OUT\_17 70 IO
- OUT\_18 69 IO
- OUT\_19 68 IO
- OUT\_20 67 IO
- OUT\_21 66 IO
- OUT\_22 91 IO
- OUT\_23 89 IO
- OUT\_24 87 IO
- OUT\_25 86 IO
- OUT\_26 61 IO
- OUT\_27 57 IO
- OUT\_28 56 IO
- OUT\_29 55 IO
- OUT\_30 54 IO
- OUT\_31 53 IO
- OUT\_32 51 IO

### Data Buffer

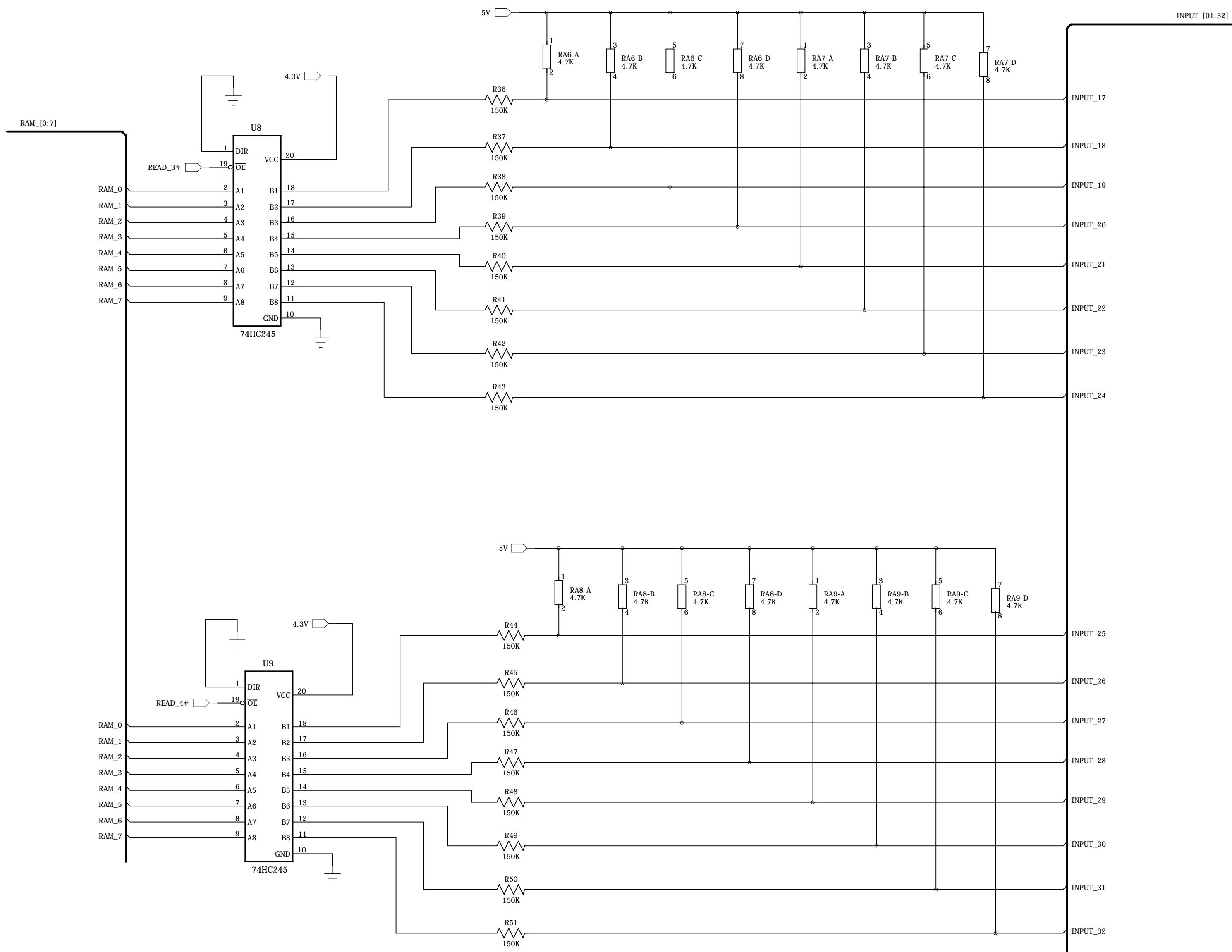


### PC/104 Bus

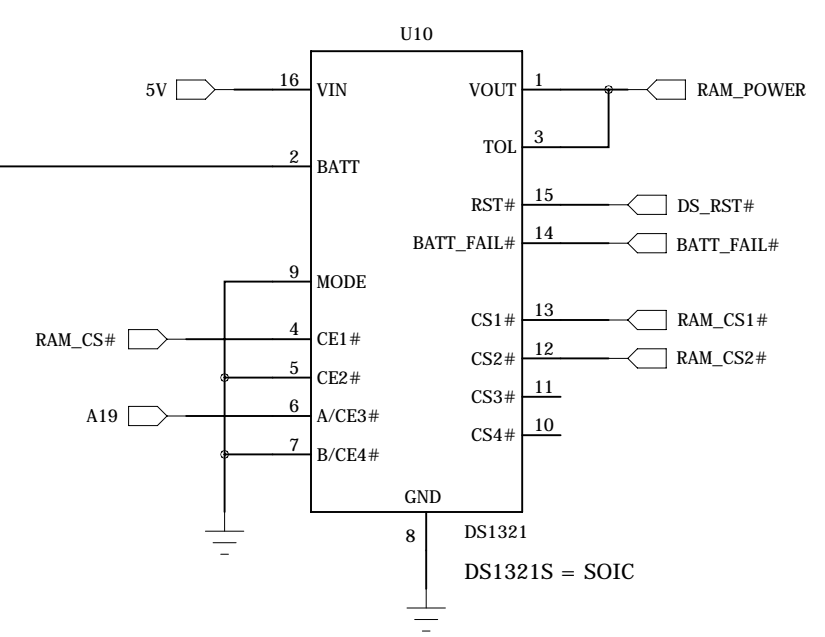
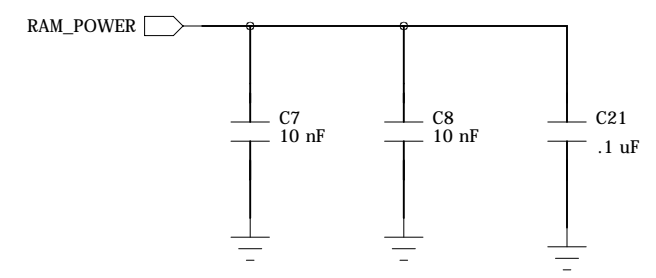




Technologic Systems		Date Dec. 31, 2005	
Title: TS-DIO64 Inputs			
Rev: 1.0	Designer RLM	Sheet 2 of 5	

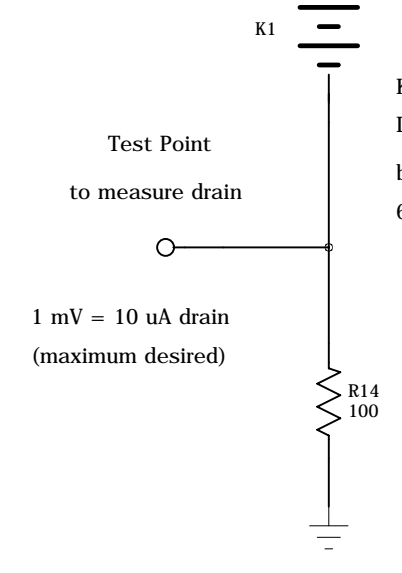


Renasas low power SRAM  
 Part # R1LP0408CSP-5SC  
 5V part



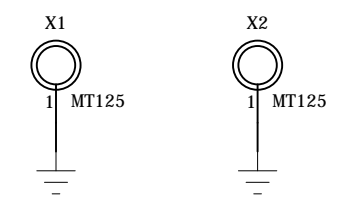
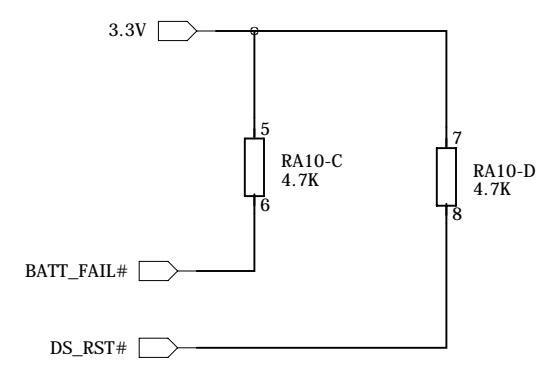
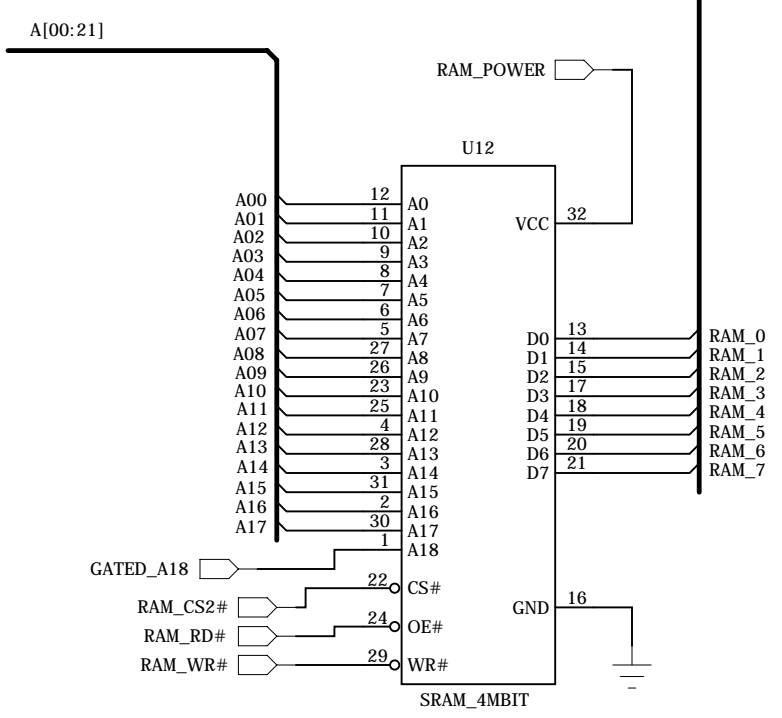
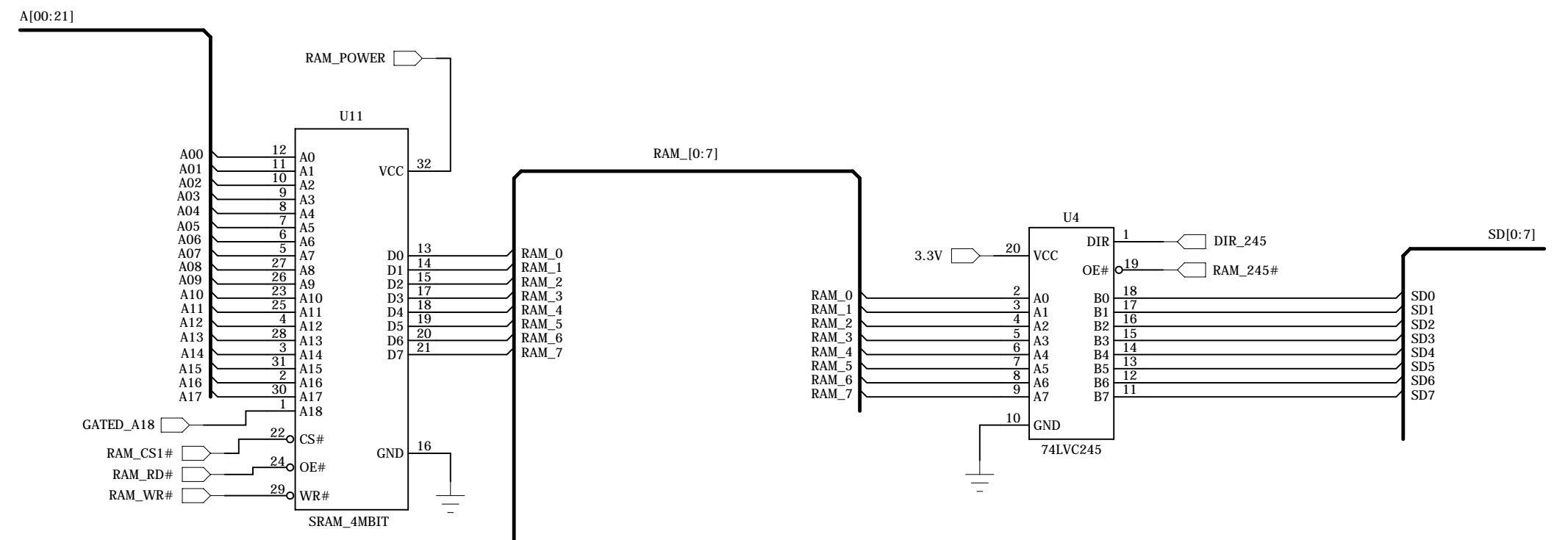
TOL tied to Vout =  
 Reset trip is 4.25 to 4.50 V  
 This is also when the battery  
 is switched in or out

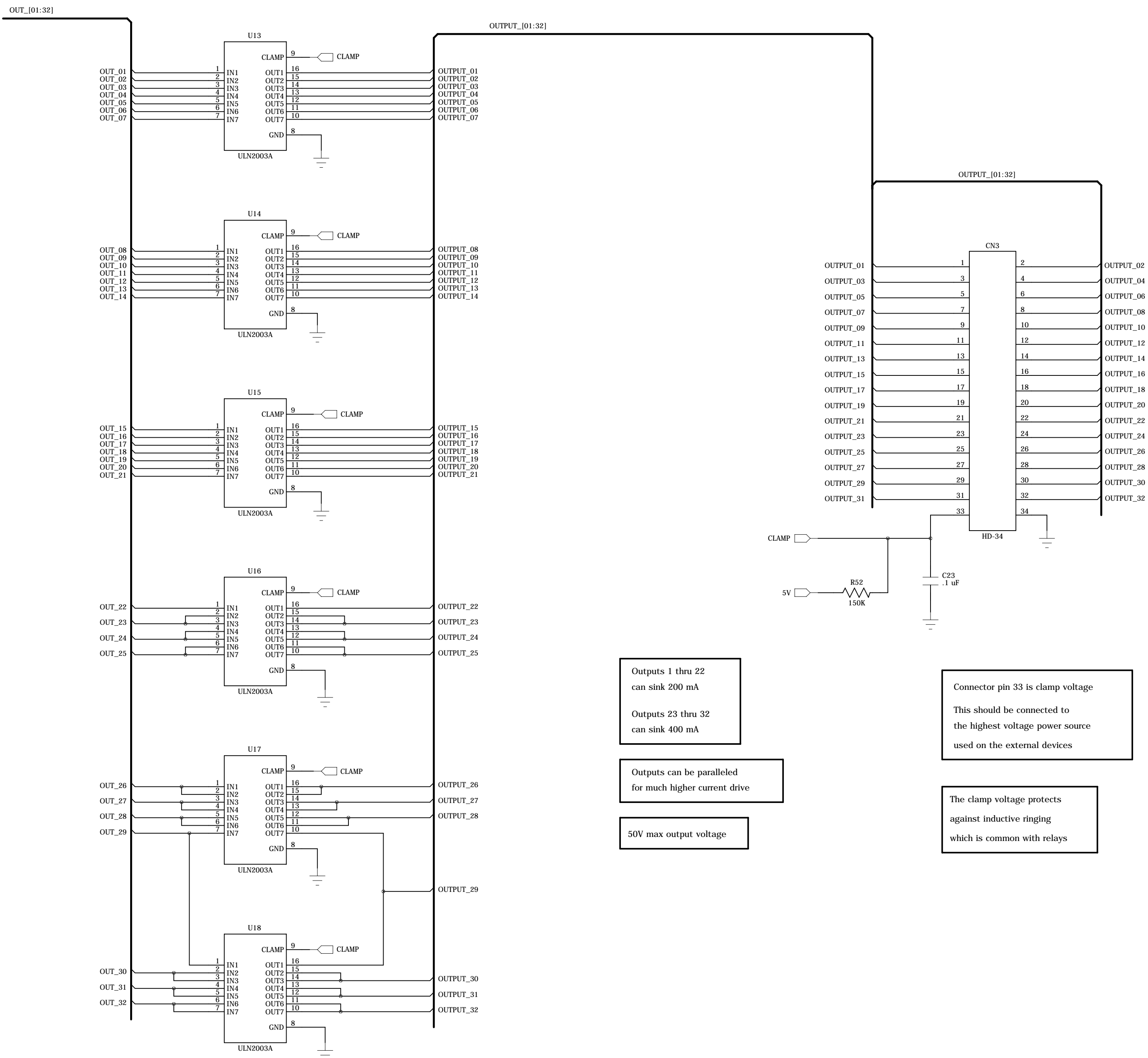
Mode = GND at power up --> 8-bit mode  
 Mode = Vout at power up --> 16-bit mode



Keystone # 1025  
 Digikey # 1025K-ND  
 battery = CR2450  
 600 mA-hour

1 mV = 10 uA drain  
 (maximum desired)





Outputs 1 thru 22  
can sink 200 mA

Outputs 23 thru 32  
can sink 400 mA

Outputs can be paralleled  
for much higher current drive

50V max output voltage

Connector pin 33 is clamp voltage  
This should be connected to  
the highest voltage power source  
used on the external devices

The clamp voltage protects  
against inductive ringing  
which is common with relays