

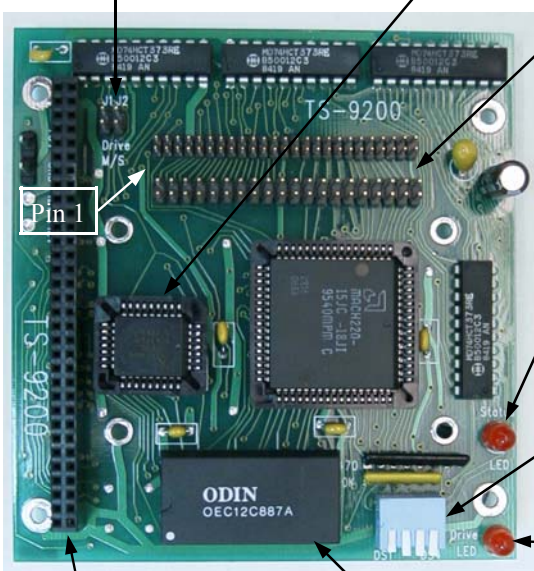
Getting started with the TS-9200

The 9200 boot board is mated via the PC104 connector to an Embedded PC (EPC). The EPC can then boot from the flash drive on the 9200 or from its own inherent flash drive. DIP switch 1 controls which flash drive the system will use to boot. The 9200 has a 512 KB flash “floppy-drive” mapped as DOS drive (A:) and an IDE interface usually mapped as DOS drive (C:). The 9200 is compatible with the TS-2100, TS-2200 and TS-2800 EPC products and is capable of saving or restoring the: flash drive, BIOS, DOS, CMOS and RTC using binary images stored in the hard drive. All programming utilities and binary images for supported products are pre-installed. (includes source code for flashup utility, and example code for EPC’s)

2.5” IDE jumper
Master/slave jumper for 2mm IDE connector.

512 kb Flash “A: drive”
Bootable flash drive if DIP switch 1 is on, otherwise unavailable.

IDE0 Connectors (40 pin std. & 2mm laptop)
Mapped as DOS drive C: or according to CMOS settings. Supports 1 or 2 standard 40 pin IDE drives or laptop drives. May be set to bootable.



Diagnostic LED
Mapped at address 0x472 bit 0. The LED is turned on by hardware by default, then turned off by software during boot-up. Available for user diagnostics after boot-up.

DIP Switch
Switch position 1 controls boot mode. Switch position 3 is available for the user. Reading address 0x477 will return the DIP switch settings in the upper nibble. (Off=0, On=1) (Do not use switches 2&4, leave off)

DIP position	4	3	2	1
0x477 bit location	7	6	5	4

Drive Access LED indicates IDE activity

8 bit PC104 Connector
Stacks above EPC.

Real Time Clock (RTC)
Battery backed real time clock and CMOS memory. Stores time and date for programming target board RTC. When booting from 9200 flash, CMOS setting changes and DOS date and time commands affect this chip. When booting from the EPC flash CMOS settings and date/time are stored in the real time clock on the EPC board.

DOS drive mappings	9200 flash drive	EPC flash drive	9200 IDE drive	EPC DiskOnChip
Boot 9200 DIP 1 on	DOS drive A: (note 1)	DOS drive B:	DOS drive C:	DOS drive D: (note 2)
Boot EPC DIP 1 off	Not available	DOS drive A: (note 1)	Not available	DOS drive C: (note 2)

Note 1: This is the 'main boot flash' which contains the BIOS and DOS images.
Note 2: The EPC DiskOnChip is optional and may not be installed.



TS-9200 with 2.5” IDE drive